EXAM 2 COVERAGE

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Exam 2 will include the following topics:

1. CLTF of a feedback system, poles, zeros, basic properties.

2. Second order system properties (settling time, maximum overshoot, rise time, etc...).

3. Design of system parameters such that one system property is given.

4. Steady state error: almost everything. You should be able to use to the SSE table and understand its meaning (system type, kind of inputs, SSE values, etc...).

5. Routh array: determining range of design parameters (such as gain K) such that the system is stable.

6. Root locus: the homework and the problems in Module 7 should be sufficient.